MICRO-700 **Advanced analog IC design**

Kayal Maher

Cursus	Sem.	Type
Electrical Engineering		Obl.
Microsystems and Microelectronics		Obl.

Language of teaching	English
Credits	2
Session	
Exam	Written & Oral
Workload	60h
Hours	**32**
Courses	32
Number of positions	

Remark

August 28 to September 1, 2017

Summary

This course is aimed at providing engineers with up-to-date information on important current issues of design in analog and mixed-mode integrated circuits. In general, the content of the lectures covers introduction, state-of-the-art in the specific field and practical case studies.

Content

Note

* Organized by MEAD/EPFL
More informations & registration at:
http://mead.ch/MEADNEW/advanced-analog-cmos-ic-design/
Contact: education@mead.ch

Resources

Ressources en bibliothèque

- Analog-to-Digital Conversion / Pelgrom
- RF analog impairments modeling for communication systems simulation : application to OFDM-based transceivers / Smaini
- Methodology for the Digital Calibration of Analog Circuits & Systems / Kayal
- All-Digital Frequency Synthesizer in Deep-Submicron CMOS / Staszewski
- Analog Design Essentials / Sansen
- Structured Analog CMOS Design / Kayal
- Charge-Based MOS Transistor Modeling: The EKV Model for Low-Power and RF IC Design / Enz
- Understanding Delta-Sigma Data Converters / Pavan
- Understanding delta-sigma data converters / Schreier