

EE-424

Fundamentals of analog VLSI design

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Cursus	Sem.	Type
Data and Internet of Things minor	H	Opt.
Electrical and Electronical Engineering	MA1, MA3	Obl.
Microtechnics	MA1, MA3	Opt.

Language of teaching	English
Credits	4
Session	Winter
Semester	Fall
Exam	Written
Workload	120h
Weeks	14
Hours	4 weekly
Lecture	2 weekly
Exercises	2 weekly
Number of positions	

Resources**Moodle Link**

- <https://go.epfl.ch/EE-424>